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Substitute for form 1449A/PTO		<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	10/619,943
		Filing Date	July 15, 2003
		First Named Inventor	Ingrid Peterson et al.
		Art Unit	2624
		Examiner Name	Kathleen S. Yuan
		Attorney Docket Number	56197200-191/P890
(Use as many sheets as necessary)			
Sheet	1	of	2

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	Date Considered
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Substitute for form 1449B/PTO				<i>Complete if Known</i>	
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				Filing Date	July 15, 2003
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				Art Unit	2624
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Sheet	2	of	2	Attorney Docket Number	

(Use as many sheets as necessary)

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
		Sahouria et al., "Full-chip Process Simulation for Silicon DRC," Mentor Graphics, March 2000, 6 pages.		
		Granik et al., "Sub-resolution process windows and yield estimation technique based on detailed full-chip CD simulation," Mentor Graphics, September 2000, 5 pages.		

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